

For Whom The Bell Tolls

Riff 1

[illegible]

Riff 2

PM								PM															
2	5	4	3	2	5	4	3	2	5	4	3	2	4	5	2								
0	3	2	1	0	3	2	1	0	3	2	1	0	2	3	0								

The diagram illustrates a 16-bit bus system. At the top, a horizontal line represents the bus, with two segments labeled "PM" (Peripheral Module) above it. Below the bus, a large rectangle is divided into three sections. The first section on the left contains a 4x8 grid of bits, with the top row being 2 5 4 3 2 5 4 3 and the bottom row being 0 3 2 1 0 3 2 1. The middle section contains a 4x8 grid of bits, with the top row being 2 5 4 3 2 4 5 4 and the bottom row being 0 3 2 1 0 2 3 2. The rightmost section is empty. A vertical line separates the first and middle sections, and another vertical line separates the middle and rightmost sections. A small "4" is written to the right of the middle section, indicating the width of the data bus.

Riff 3

[illegible]

Riff 4

[illegible]

Riff 5

[illegible]

Riff 4

PM				PM				PM				PM				PM				PM												
2	2	2	5	2	2	2	2	2	2	2	5	3	1	4	2	2	2	5	2	2	2	2	2	2	2	5	2	2	3	1	4	3
0	0	0	3	0	0	0	0	0	0	0	3	0	0	2	0	0	0	3	0	0	0	0	0	0	0	3	0	0	0	0	2	1